

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: M. OGINO, et al.  
Application No.: 09/429,297  
Filed: October 28, 1999  
For: SEMICONDUCTOR DEVICE, SEMICONDUCTOR WAFER,  
SEMICONDUCTOR MODULE, AND A METHOD OF  
MANUFACTURING SEMICONDUCTOR DEVICE  
Group: 2827  
Examiner: J. Mitchell

RESPONSE TO NOTICE REGARDING DRAWINGS

Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

January 6 2005

Sir:

In response to the Notice Regarding Drawings mailed December 6, 2004,

Applicants submit herewith corrected drawings for the above-identified application.

The corrections to the drawings are the same as the corrections made in the drawings filed with the Amendment on March 31, 2000, and it is respectfully submitted that the corrections to the drawings clearly do not constitute new matter.

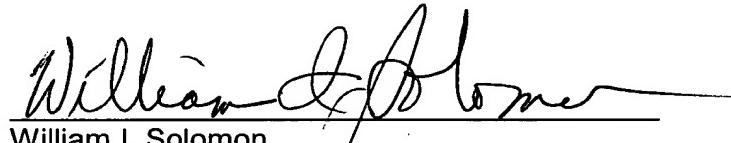
Entry of the corrected drawings and passing of the above-identified application to issue in due course are respectfully requested.

503.37770X00

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to the Deposit Account No. 01-2135 (Case No. 503.37770X00), and please credit any excess fees to such Deposit Account.

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP



William I. Solomon  
William I. Solomon  
Registration No. 28,565

1300 North 17<sup>th</sup> Street  
Suite 1800  
Arlington, VA 22209  
Tel: (703) 312-6600  
Fax: (703) 312-6666  
WIS/sjg